

<<Cryptographic Hardwa>>

图书基本信息

书名：<<Cryptographic Hardware and Embedded Systems - CHES 2005加密硬件与嵌入系统 - CHES 2005 / 会议录>>

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内容概要

This book constitutes the refereed proceedings of the 7th International Workshop on Cryptographic Hardware and Embedded Systems, CHES 2005, held in Edinburgh, UK in August/September 2005. The 32 revised full papers presented were carefully reviewed and selected from 108 submissions. The papers are organized in topical sections on side channels, arithmetic for cryptanalysis, low resources, special purpose hardware, hardware attacks and countermeasures, arithmetic for cryptography, trusted computing, and efficient hardware.

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书籍目录

Side Channels Resistance of Randomized Projective Coordinates Against Power Analysis Templates as Master
 Keys A Stochastic Model for Differential Side Channel Cryptanalysis Arithmetic for Cryptanalysis A New
 Baby-Step Giant-Step Algorithm and Some Applications to Cryptanalysis Further Hidden Markov Model
 Cryptanalysis Low Resources Energy-Efficient Software Implementation of Long Integer Modular Arithmetic
 Short Memory Scalar MultiDlication on Koblitz Curves Hardware/Software Co-design for Hyperelliptic Curve
 Cryptography(HECC) on the 8051 uP Special Purpose Hardware SHARK: A Realizable Special Hardware
 Sieving Device for Factoring 1024-Bit Integers Scalable Hardware for Sparse Systems of Linear Equations, with
 Applications to Integer Factorization Design of Testable Random Bit Generators Hardware Attacks and
 Countermeasures I Successfully Attacking Masked AES Hardware Implementations Masked Dual-Rail
 Pre-charge Logic: DPA-Resistance Without Routing Constraints Masking at Gate Level in the Presence of
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